

SILICON TRANSISTOR 2SC1623

NPN SILICON EPITAXIAL TRANSISTOR MINI MOLD

FEATURES

• High DC Current Gain:

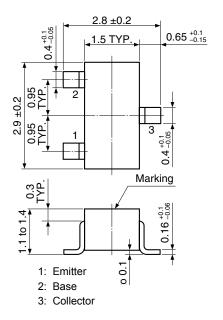
 $h_{FE} = 200 \text{ TYP.} (V_{CE} = 6.0 \text{ V}, I_{C} = 1.0 \text{ mA})$

• High Voltage: VcEo = 50 V

ABSOLUTE MAXIMUM RATINGS

Maximum Voltages and Current ($T_A = 25^{\circ}C$) Collector to Base Voltage Vсво 60 Collector to Emitter Voltage VCEO 50 Emitter to Base Voltage VEBO 5.0 Collector Current (DC) lс 100 mΑ Maximum Power Dissipation **Total Power Dissipation** at 25°C Ambient Temperature 200 mW Maximum Temperatures 150 °С Junction Temperature Τį Storage Temperature Range Tstg -55 to +150 °C

★ PACKAGE DRAWING (Unit: mm)



ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cutoff Current	Ісво			0.1	μΑ	V _{CB} = 60 V, I _E = 0 A
Emitter Cutoff Current	I _{ЕВО}			0.1	μΑ	V _{EB} = 5.0 V, I _C = 0 A
DC Current Gain	hfe	90	200	600		Vce = 6.0 V, Ic = 1.0 mA ^{Note}
Collector Saturation Voltage	V _{CE(sat)}		0.15	0.3	V	Ic = 100 mA, I _B = 10 mA ^{Note}
Base to Saturation Voltage	V _{BE(sat)}		0.86	1.0	V	Ic = 100 mA, I _B = 10 mA ^{Note}
Base to Emitter voltage	VBE	0.55	0.62	0.65	V	Vce = 6.0 V, Ic = 1.0 mA ^{Note}
Gain Bandwidth Product	f⊤		250		MHz	Vce = 6.0 V, I _E = -10 mA
Output Capacitance	Cob		3.0		pF	V _{CE} = 6.0 V, I _E = 0 A, f = 1.0 MHz

Note Pulsed: PW \leq 350 μ s, Duty Cycle \leq 2%

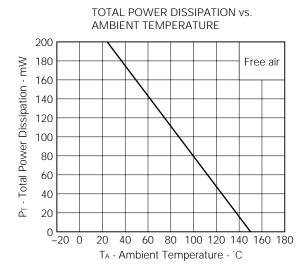
hfe CLASSIFICATION

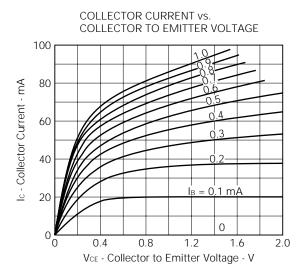
Marking	L4	L5	L6	L7
hfE	90 to 180	135 to 270	200 to 400	300 to 600

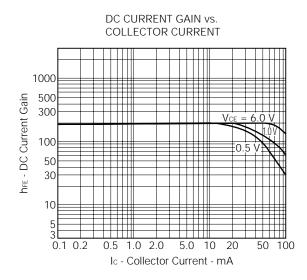
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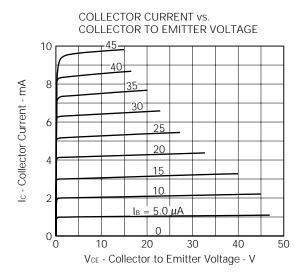


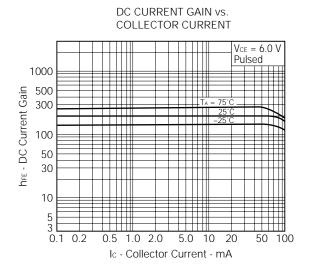
TYPICAL CHARACTERISTICS (TA = 25°C)



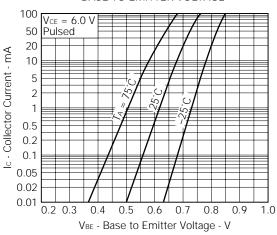




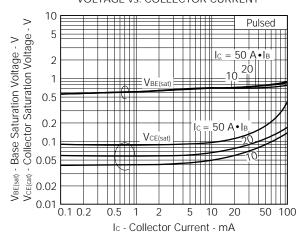




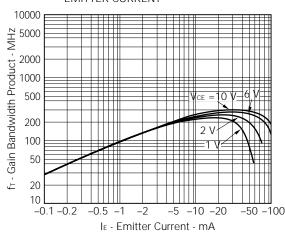
COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



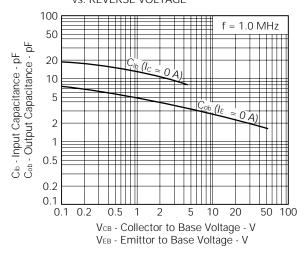
COLLECTOR AND BASE SATURATION VOLTAGE vs. COLLECTOR CURRENT



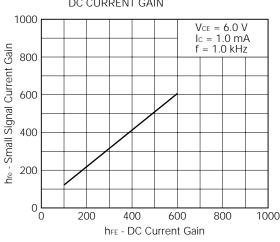
GAIN BANDWIDTH PRODUCT vs. EMITTER CURRENT



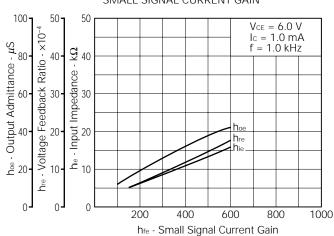
INPUT AND OUTPUT CAPACITANCE vs. REVERSE VOLTAGE



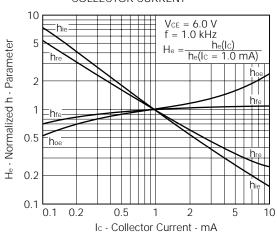
SMALL SIGNAL CURRENT GAIN vs. DC CURRENT GAIN



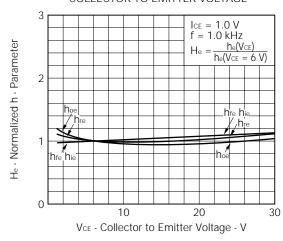
INPUT IMPEDANCE VOLTAGE FEEDBACK RATIO AND OUTPUT ADMITTANCE vs. SMALL SIGNAL CURRENT GAIN



NORMALIZED h-PARAMETER vs. COLLECTOR CURRENT



NORMALIZED h-PARAMETER vs. COLLECTOR TO EMITTER VOLTAGE



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